



CHADALAWADA RAMANAMMA ENGINEERING COLLEGE

(AUTONOMOUS)

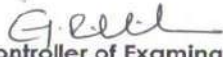
Chadalawada Nagar, Renigunta Road, Tirupati – 517 506, A.P.


EXAMINATION BRANCH

M. Tech I Year II Semester (R17) Regular & Supplementary Examinations, July - 2019

Time: 02.00 PM to 05.00 PM

Date / Day	Power Electronics & Drives (PE&D)	CAD/CAM	VLSI System Design (VLSISD)	Computer Science & Engineering (CSE)
27.07.2019 (Saturday)	Advanced Power Converters (17CD02201)	Advanced Optimization Techniques (17CD03201)	CPLD and FPGA Architecture Applications (17CD04201)	Object Oriented Analysis and Design (17CD05201)
29.07.2019 (Monday)	Power Quality (17CD02202)	Industrial Robotics & Expert Systems (17CD03202)	Testing & Testability (17CD04202)	Big Data Analytics (17CD05202)
31.07.2019 (Wednesday)	Advanced Drives & Control (17CD02203)	CNC Technology & Programming (17CD03203)	CMOS Mixed Signal Design (17CD04203)	Internet of Things (17CD05203)
03.08.2019 (Saturday)	Renewable Energy Conversion Systems (17CD02204)	Mechatronics Applications in Manufacturing (17CD03204)	Algorithms for VLSI Design Automation (17CD04204)	Mobile Application Development (17CD05204)
05.08.2019 (Monday)	HVDC & EHVAC Transmission Systems (17CD02207)	Special Manufacturing Process (17CD03206)	Semiconductor Memory Design and Testing (17CD04206)	Human Computer Interaction (17CD05206)
07.08.2019 (Wednesday)	Energy Efficient Electrical Systems (17CD02209)	Global Integrated Manufacturing (17CD03209)	VLSI Process Technology (17CD04209)	Cloud Computing (17CD05211)


Addl. Controller of Examinations
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