

# CHADALAWADA RAMANAMMA ENGINEERING COLLEGE

(AUTONOMOUS)

Chadalawada Nagar, Renigunta Road, Tirupati - 517 506, A.P.

EXAMINATION BRANCH

B. Tech II Year I semester (R17) I CIA Examinations, August 2019

Time: 10.30 AM to 12.30 PM

Date / Day	Electrical & Electronics Engineering (EEE)	Mechanical Engineering (ME)	Computer science & Engineering (CSE)	Electronics & Communication Engineering (ECE)
26.08.2019 (Monday)	Mathematics -III (17CA54301)		Probability and Statistics(17CA54302)	Complex analysis and Partial Differential Equations (17CA54303)
27.08.2019 (Tuesday)	Electronic Devices and Circuits (17CA04301)	Thermodynamics (17CA03301)	Digital Logic Design (17CA04306)	Electronic Devices & Circuits (17CA04301)
28.08.2019 (Wednesday)	DC Machines (17CA02301)	Engineering Mechanics (17CA03302)	Discrete Mathematical Structures(17CA05301)	Switching Theory and Logic Design(17CA04302)
29.08.2019 (Thursday)	Electrical Circuits-II (17CA02302)	Manufacturing Technology (17CA03303)	Database Management Systems (17CA05302)	Probability Theory & Stochastic Processes (17CA04303)
30.08.2019 (Friday)	Electrical Measurements and Instrumentation (17CA02303)	Managerial Economics and Financial Analysis (17CA53301)		Signals and Systems (17CA04304)
31.08.2019 (Saturday)	Electromagnetic Field Theory (17CA02304)	Engineering Drawing for Mechanical Engineers(17CA03304)	Basic Electrical and Electronics Engineering (17CA02308)	Basic Electrical Technology (17CA02306)

*G. Ell*  
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*R. Lohanan*  
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